What is Claimed is:

1. An apparatus for compensating for offset and drift of offset in an amplifier, comprising:

an amplifier circuit having metal oxide semiconductor transistors in an input stage thereof and including a node responsive to a bias to change the offset of the amplifier circuit;

an offset digital-to-analog converter providing a first programmable bias corresponding to an offset of the amplifier circuit; and

a drift digital-to-analog converter providing a second programmable bias corresponding to a drift of the offset of the amplifier circuit;

the first programmable bias and the second programmable bias being combined and coupled to the node.

2. A method for compensating for offset and drift of offset in an amplifier circuit having metal oxide semiconductor transistors in an input stage thereof and including a node responsive to a bias to change the offset of the amplifier circuit, comprising the steps of:

providing a first programmable bias corresponding to an offset of the amplifier circuit;

providing a second programmable bias corresponding to a drift of the offset of the amplifier circuit;

combining the first programmable bias and the second programmable bias and coupling the combined biases to the node.

3. An apparatus for compensating for offset and drift of offset in an amplifier, comprising:

an amplifier circuit having metal oxide semiconductor transistors in an input stage thereof and including a node responsive to a bias to change the offset of the amplifier circuit;

a first programmable offset/drift generator capable of sourcing a first bias to the amplifier node compensating for a first portion of the offset and a first portion of the drift of the offset of the amplifier circuit; and

a second programmable offset/drift generator capable of sourcing a second bias to the amplifier node compensating for a second portion of the offset and a second portion of the drift of the offset of the amplifier circuit, wherein the rate of drift compensation with temperature of the second bias is different from the rate of compensation of the second bias, such that by suitable programming of the first and second programmable offset/drift generators the compensation of the offset and the offset of the drift of the amplifier circuit may be optimized.

4. A method for compensating for offset and drift of offset in an amplifier circuit having metal oxide semiconductor transistors in an input stage thereof and including a node responsive to a bias to change the offset of the amplifier circuit, comprising the steps of:

providing a first programmable bias to the amplifier node compensating for a first portion of the offset and a first portion of the drift of the offset of the amplifier circuit; and

providing a second programmable bias to the amplifier node compensating for a second portion of the offset and a second portion of the drift of the offset of the amplifier circuit; and

controlling the rate of drift compensation with temperature of the first bias and the second bias such that the second bias is different from the rate of compensation of the second bias and the compensation of the offset and the offset of the drift of the amplifier circuit is optimized.

5. A method for compensating for offset and drift of offset in an amplifier circuit having metal oxide semiconductor transistors in an input stage thereof

and including a node responsive to a bias to change the offset of the amplifier circuit, comprising the steps of:

measuring a first offset error at a first temperature;

storing the first offset error in a first memory;

measuring a second offset error at a second temperature;

storing the second offset error in the first memory;

computing drift and offset at the first temperature, based on the first offset error;

computing an offset compensation code;

storing the offset compensation code in a second, non-volatile memory;

computing an offset drift compensation code;

storing the offset drift compensation code in the second memory; and upon activation of the amplifier circuit:

retrieving the offset compensation code and the offset drift compensation code;

responsive to the retrieved offset compensation code, providing a first programmable bias corresponding to an offset of the amplifier circuit;

responsive to the retrieved offset drift compensation code, providing a second programmable bias corresponding to a drift of the offset of the amplifier circuit;

combining the first programmable bias and the second programmable bias and coupling the combined biases to the node.